

# **SOUTH GUJARAT UNIVERSITY, SURAT**

## **FACULTY OF TECHNOLOGY INCLUDING ENGINEERING BOARD OF STUDIES IN INSTRUMENTATION & CONTROL ENGINEERING**

**Proposed Teaching and Examination Scheme and Detailed Syllabus**

**for**

**B.E.II (INSTRUMENTATION AND CONTROL ENGINEERING)**

**1998**

**Scheme For Teaching and Examination  
B.E.-II (Instrumentation & Control Eng. ) 3<sup>rd</sup> Semester**

Course	Course No	Teaching Scheme			Examination Scheme					
		L	T	P	Theory Exam University Exam		Pract/Quiz/Viva/T.W. ect.			
		(Hrs)	(Hrs)	(Hrs)	Duration (Hrs)	Marks	Tutorial Marks	Cont Int.Evalu ation (P)	Examinat ion (P) Marks	Total Marks
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)
Engineering Maths	ASH 301 IC	3	1	0	3	100	25	-	-	25
Electrical Machines	ELE 302 IC/EC	3	0	2	3	100	-	20	30	50
Electrical Circuit Theory	ELE 303 IC	3	1	0	3	100	25	-	-	25
Measurement & Instruments	IC 304 IC	3	0	2	3	100	-	20	30	50
Digital Circuit	EC 305 IC	3	1	2	3	100	25	20	30	75
Electronic Devices And Circuits	EC 306 IC/EC	3	1	2	3	100	25	20	30	75
	(Total)	18	4	8		600	100	80	120	300
Total contact Hours=				30				Grand	Total	900

**Scheme For Teaching and Examination  
B.E.-II (Instrumentation & Control Eng.) 4<sup>th</sup> Semester**

Course	Course No	Teaching Scheme			Examination Scheme					
		L	T	P	Theory Exam University Exam		Pract/Quiz/Viva/T.W. ect.			
		(Hrs)	(Hrs)	(Hrs)	Duration (Hrs)	Marks	Tutorial Mark s	Cont Int.Ev aluati on (P)	Examinat ion (P) Marks	Total Marks
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)
Computational methods Using C	CO 401 IC	3	0	2	3	100	-	20	30	50
Pulse & switching Ckt.	EC 402 IC	3	1	2	3	100	25	20	30	75
Properties of Eng. material	IC 403 IC	3	0	0	3	100	-	-	-	-
Electronic Circuits	EC 404 EC/IC	3	1	2	3	100	25	20	30	75
Control theory	IC 405 IC	3	1	0	3	100	25	-	-	25
Digital Integrated Ckt.	EC 406 IC	3	1	2	3	100	25	20	30	75
	(Total)	18	4	8		600	100	80	120	300
Total contact Hours=				30				Grand	Total	900

**ENGINEERING MATHS : ASH 301 IC**  
**B.E.II (IC) 3rd semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	0
Examination Scheme Marks	100	25	Cont. Evaluation 00 Examination 00

1	MULTIPLE INTEGRALS : Reorientation of concept of integrals, double and triple integration techniques, change of order of integration, integrals in polar and cylindrical coordinates, change of variables of multiple integrals, Application of double and triple integrals for evaluation of area, volume and mass.
2	VECTOR CALCULUS : Basic concepts of Vector calculus, line integrals, scalar and vector point functions, differential operator, gradient, directional derivative, divergence, curl and Laplacian with their properties and physical interpretation.
3	Surface integrals, Green's, Gauss and Stokes theorem (without proof), Applications.
4	GAMMA, BETA AND ERROR FUNCTIONS : Improper integrals and their convergence, Gamma and Beta functions and their properties, Error functions, Evaluation and application.
5	FOURIER SERIES : Fourier expansion of functions with arbitrary period, in particular periodic functions with period $2p$ , conditions of convergence, Fourier series of even and odd functions, Half range Fourier series.
6	PARTIAL DIFFERENTIAL EQUATIONS (PDE) : Basic mathematical concepts, First order PDE of Lagrange's form, $Pp + Qq = R$ , Second order PDE of mathematical Physics (Heat, Wave and Laplace equation) with standard boundary conditions, Solution by separation of variable method using Fourier Series. Partial differential equation Modeling.
7	LAPLACE TRANSFORMATION Basic concept of laplace transform, Laplace of different function, Inverse laplace transform, It's application in 1st & 2nd order system.
8	COMPLEX VARIABLES : Basic mathematical concepts, Analytic functions, C-R equations, Harmonic functions, Related problems; Linear transformations of complex domains, Some special transformations, bilinear transformation, Conformal Mapping and applications; complex integration including contour Integration (Simple cases).

TUTORIALS: Tutorials shall be based upon theory.

REFERENCES :

1. E. KREYSZIG : Advanced Engineering Mathematics, John Wiley, International Student Ed. (1995).
2. C. R. WYLLE : Advanced Engineering Mathematics, Mc-Graw Hill, International Student Ed. (1993).
3. GREWAL B.S.: Higher Engineering Mathematics, khanna publication (1995)

**ELECTRONIC DEVICES & CIRCUITS : EC 302 EC/IC**  
**B.E.II (IC) 3rd semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	2
Examination Scheme Marks	100	25	Cont. Evaluation 20 Examination 30

1	DIODE CIRCUIT ANALYSIS : Introduction to Semiconductor junction diode; Nonlinear properties; Ideal diode; Basic theory and analysis of simple diode circuit; DC load line; Small signal analysis and concept of dynamic resistance; AC load line; Diode capacitance; Temperature effects of diode; Different types of diode (Zener, schottky) Manufacturer's specifications.
2	RECTIFIERS : Circuit analysis of half wave and full wave rectifier using semiconductor devices; Bridge rectifier; Ripple and form factor calculation for above circuits; Efficiency and PIV for above circuits; Types of filters; C filter, L filter, LC filter, PIE filter; Analysis of filter and calculation of ripple and regulation.
3	INTRODUCTION TO TRANSISTOR CIRCUITS : Transistor characteristic; Plots for NPN and PNP configurations; Current flow mechanism in the junction transistor and calculation of Alpha and Beta; Analysis of CE configuration; Current amplification in the transistor; Graphical analysis of transistor circuits; Power calculations; Infinite bypass capacitor; Infinite coupling capacitors; Different dc biasing methods; Fixed bias, emitter stabilized bias, potential divider bias, dc bias with voltage feedback; Common base configuration analysis; Emitter follower.
4	BIAS STABILITY : Quiescent point variation due to uncertainty in Beta; Effect of temperature on the Q point; Stability factor analysis; Temperature compensation using diode biasing.
5	AUDIO FREQUENCY LINEAR POWER AMPLIFIERS : Introduction to Class A, B, AB and C operation; Class A common-emitter power amplifier; Transformer coupled amplifier; Class B push-pull power amplifier; Amplifiers using complementary symmetry; Class C amplifier.
6	SMALL SIGNAL LOW FREQUENCY ANALYSIS AND DESIGN : Hybrid parameters; CE configuration; CB configuration; CC configuration; Impedance reflection; Bootstrapping circuit; Phase splitter; Interpretation of manufacturers specification
7	MULTIPLE TRANSISTOR CIRCUITS : Cascading of amplifier stages; Difference amplifier; Common mode rejection ratio; Difference amplifier with constant current source; Different amplifier with emitter resistor for balance; Darlington amplifier; Cascade amplifier

TUTORIALS : Tutorials shall be based on Theory

REFERENCES :

1. SCHILLING & BELOVE : Electronic Circuits-Discrete and Integrated McGraw-Hill Pub., 3rd edition 1989, Reprint 1994.
2. BOYLESTEAD & NASHLESKY : Electronic Devices & Circuit Theory, Prentice-Hall India, 5th edition 1992, Ninth reprint 1995.
3. SEDRA & SMITH : Microelectronics Circuits, 4th Edition, 1996.
4. HORENSTEIN : Microelectronics Circuits & Devices, Prentice-Hall of India, 1996.

**ELECTRICAL CIRCUIT THEORY: EL 303 IC**  
**B.E.II (IC) 3rd semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	0
Examination Scheme Marks	100	25	Cont. Evaluation 00 Examination 00

1.	<b>NETWORK CONCEPTS :</b> Network element Symbols and Conventions - Active element Conventions -Voltage & current Conventions - Loops and Meshes -nodes, Coupled circuit and dot Conventions.
2.	<b>MESH CURRENT NETWORK ANALYSIS :</b> -Mesh current - choice of Mesh Current, - Number of Mesh currents required in setting up mesh equations. By inspection -Self & Mutual inductance - impedance matrix form, Use of cramer's rules for Analysis.
3.	<b>NODE VOLTAGE NETWORK ANALYSIS:</b> - Nodal Analysis for setting up Voltage equation.- Self and Mutual admittance,- Cramer's rules and Matrices Method for Nodal analysis in Voltage form.
4.	<b>THEOREMS OF NETWORK:</b> - Linearity & Superposition with Application.- Thevenin's. theorems and Applications.- Norton's. theorems and Applications.- Miller's. theorems and Applications.- Max Power transfer , reciprocity theorem , Duality and dual of planner network.
5.	<b>LAPLACE TRANSFORMATION FOR ANALYSIS :-</b> - R-L, R-C and R-L-C DC transients, - Two mesh transient,- R-L, R-C & R-L-C Sinusoidal transients analysis using Laplace transform.- Initial & final Value theorem for dc and ac Analysis.
6.	<b>GRAPH THEORY AND ITS APPLICATION :</b> -Fundamental concepts . Definition and relative terms.-tree topology of graph :-planner & dual graphs.-Matrices of oriented graphs, and this properties
7.	<b>SINUSOIDAL STEADY STATE ANALYSIS :</b> -Radian freq. and sinusoid -Magnitude and phase of n/w fan -Sinusoidal n/w function in terms of poles an zeros.-Resonant circuit bandwidth and factor -Polar plot of n/w .applications of symmetrical lattice networks.

**TUTORIALS** : Tutorials shall be based on Theory

**REFERENCES :**

- (1) VAN VALKENBERG : Network Analysis. Printice hall of India (1996)
- (2) VAN VALKENBERG : network synthesis, Printice Hall of India (1996)
- (3) G.K. MITHAL : network theory. Khanna Publication, (1995)
- (4) HAYT AND KIMMERLY : Eng. Circuit analysis. Mc Graw Hill Publication (1993)
- (5) IYER TSRV: Circuit analysis . Tata McGraw Hill (1994)

**MEASUREMENT & INSTRUMENTS : IC 304 IC**  
**B.E.II (IC) 3rd semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	0	2
Examination Scheme Marks	100	0	Cont. Evaluation 20 Examination 30

1	UNITS AND DIMENSIONS Absolute Units, Fundamental and Derived Units, Dimensional Equation, CGS & MKS System, SI Units, Determination of Absolute Units, Non electrical units
2	CHARACTERISTIC OF MEASURING INSTRUMENT:- Types of static and dynamic characteristic, Types of errors in measuring instrument and its reduction, Types of systems for measurement (1st order, 2nd order etc.)
3	Measurement of Resistance ,Inductance and Capacitor Using Meter And Bridges using Different Techniques
4	INDICATING AND INTEGRATING INSTRUMENTS:- Principles of moving iron , moving coil and rectifier type ammeter and voltmeters-principle of dynamic watt meter and induction energy meter , Principle of P.F. meter and frequency meter.
5	INSTRUMENT TRANSFORMERS:- Theory of current and potential transformers -ratio error and phase angle-Use of CT/PT for extension of instruments range-testing of CT/PT , Biffi's method and clothier-Median method.
6	MEASURING INSTRUMENT : Analog type : classifications and operating principle (i) Galvanometers --Working & Construction (ii) Multimeter Working & construction and App. (iii) C.R.O -- Types, Construction & Working Principle, Application for voltage, current , Phase difference and lissajous pattern
7	DIFFERENT TYPES OF CABLES, IT'S APPLICATION:
8	INTRODUCTION TO POWER TRANSMISSION AND DISTRIBUTION SYSTEM:

REFERENCES:

- 1) A.K. SAWHNEY :-Course in Electrical and electronic Measurement and Instrumentation Dhanpat rai and sons.. (1996)
- 2) COPPER W. D.& HELFRIC A. D.: Modern electronic instrumentation and measurements techniques, Prentice Hall of India Ltd, (1996)
- 3) GOLDING E. W. AND WIDDIS F.C. : Electrical Measurements & Measuring Instruments ,Wheeler publication, (1995)
- 4) BUCKINGHAM & PRICE: Principles of Electrical Measurement, ELBS Book, (1993)
- 5) V.K., MEHTA: Principle of power System Including generation , transmission,. distribution, switch gear and protection, S. Chand & Company Ltd.(1996)

**DIGITAL CIRCUITS : EC 305 EC/IC**  
**B.E.II (IC) 3rd Semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	2
Examination Scheme Marks	100	25	Cont. Evaluation 20 Examination 30

1	<u>NUMBER SYSTEMS</u> : Decimal number system; Binary, octal and hexadecimal number systems; Conversion from one number to another number system; Addition, subtraction, multiplication and division using different number systems; Representation of binary number in sign-magnitude, sign 1's complement and sign 2's complement notation; Rules for addition and subtraction with complement representation; BCD, EBCDIC, ASCII, Extended ASCII, Gray and other codes.
2	<u>LOGIC GATES AND BOOLEAN ALGEBRA : AND, OR, NOT, NAND, NOR, Ex-OR logic gates:</u> Positive and negative logic; Fundamental concepts of Boolean algebra; Demorgan's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean functions; SOP and POS forms; Realization of Boolean functions using only NAND and NOR gates.
3	<u>BOOLEAN FUNCTION MINIMIZATION</u> : Objectives of the minimization procedures; Karnaugh map method; Don't care conditions; Quinn-McCluskey tabulation method; Concept of prime implicants
4	<u>COMBINATIONAL LOGIC CIRCUITS USING DESECRATE LOGIC GATES</u> : Half adder and full adder; Half subtractor and full subtractor; Parity generator and checker; Code converters; Binary multiplier; Majority circuits, magnitude comparator.
5	<u>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS</u> : Binary parallel adder; BCD adder; Encoder, priority encoder, decoder; Multiplexer and Demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; Arithmetic and logic unit; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory, Read only memory and erasable programmable ROMS; Programmable logic array (PLA) and programmable array logic (PAL).
6	<u>INTRODUCTION TO SEQUENTIAL LOGIC CIRCUITS</u> : Basic concepts of sequential circuits; Cross coupled SR flip-flop using NAND or NOR gates; JK flip-flop rise condition; Clocked flip-flop; D-type and Toggle flip-flops; Truth tables and excitation tables for flip-flops; Master slave configuration; Edge triggered and level triggered flip-flops; Elimination of switch bounce using flip-flops; Flip-flops with preset and clear.
7	<u>SEQUENTIAL LOGIC CIRCUIT DESIGN</u> : Basic concepts of counters and registers; Binary counters; BCD counters; Up down counter; Johnson counter, module-n counter; Design of counter using state diagrams and table; Sequence generators; Shift left and right register; Registers with parallel load; Serial-in-parallel-out(SIPO) and parallel-in-serial-out(PISO); Register using different type of flip-flop; Sequence generator.

**TUTORIALS** : Tutorials shall be based on Theory

**REFERENCES** :

1. MORRIS MANO : Digital Logic and Computer Design, Prentice-Hall of India, New Delhi, 1992.
2. BARTEE THOMAS : Digital Computer Fundamentals, McGraw- Hill, 1995.
3. TAUB AND SCHILLING : Digital Integrated Electronics, McGraw- Hill, 1985.
4. RICHARD SANDIGE : Modern Digital Design, McGraw-Hill, 1990.

**ELECTRICAL MACHINES : EL 306 EC/IC**  
**B.E.II (IC) 3rd semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	0	2
Examination Scheme Marks	100	-	Cont. Evaluation 20 Examination 30

1	MACHINES: Construction, simple lap and wave winding , emf torque and power equations, Ckt model, Generating and motoring models , Magnetizing characteristics, Introduction to armature reaction and commutation, Self excited generator, Shunt and series compound motors , Speed control, efficiency and losses.
2	TRANSFORMERS :Fundamentals and construction of single phase and three phase transformers, , ideal transformer, emf equation, no load conditions, Loading accounting for finite permeability and core losses equivalent circuit, no load and short circuit test per unit system, Voltage regulation, Efficiency, Auto-transformer ,three phase transformer, Star and delta connections.
3	SYNCHRONOUS MACHINES : construction and basic principals, three phase windings, rotating magnetic field, distribution and pitch factors, emf equation, synchronous speed, armature reaction. Synchronous reactance, voltage regulation, synchronizing to mains, damper winding, vector diagram for generating and motoring modes, synchronous motor starting. V curves.
4	INDUCTION MACHINES : construction and simple theory of operation of three phase induction motor. equivalent circuit, torque speed characteristics, no load and blocked rotor tests, load test, starting, speed control.
5.	FRACTIONAL KW MOTORS : Brief description of reluctance motor hysteresis motor, two phase servo motor, stepper motors.

TUTORIALS : Tutorials shall be based on Theory

REFERENCES:

1. Clayton A.E.: Performance and design of DC Machines, CBS Publication, (1990)
2. Say MG: Performance and design of AC machines. Pitman. CBS Publication, (1983)
3. Nagrath I.J. & Kothari D.P.Electric Machines,. TMH.(1996)
4. Mukherjee and Chakravorti: Electrical Machines, Dhanpat Rai & sons, (1996)

**COMPUTATIONAL METHODS USING C CO 401 IC  
BE II ( IC ) 4 th Semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	0	2
Examination Scheme Marks	100	0	Cont. Evaluation 20 Examination 30

1	ERROR ANALYSIS AND SOLUTIONS OF NON LINEAR EQUATIONS : Error, their types, estimation, Accuracy And Stability, Bisection Method, Method Of False Position., Newton Raphson Method, Secant Method.
2	INTERPOLATION : Finite Difference Operators and Difference Tables , Newton-Gregory Formulas ( Forward, Backward & Divided ), Langrange's Interpolation Formula.
3	NUMERICAL INTEGRATION AND DIFFERENTIATION : Trapezoidal Formula For A Single Interval , Simpson's 1/3 and 3/8 formulas, Numerical Derivatives using Newton forward difference formula.
4	MATRICES : Matrix Inversion, Determinant Of A Matrix, Gauss Elimination, Gauss-Jordan Method, LU Decomposition, Eigen Value And Eigen Vectors, Power Method, Iterative methods - Jacobi and Gauss-Seidel methods.
5	NUMERICAL SOLUTION OF ORDINARY AND PARTIAL DIFFERENTIAL EQUATIONS : Taylor's Series, Euler's Method, Runge Kutta 2 and 4 order Methods For Ordinary Differential Equations, Initial Value And Boundary Value Problems, Gauss-Seidel And Jacobi Methods For Partial Differential Equations

REFERENCES :

- 1 SASTRY S. S : Introductory Methods Of Numerical Analysis PHI 1994
- 2 SALARIA R S : Numerical Methods - A Computer Oriented Approach BPB 1996

**ELECTRONIC CIRCUITS: EC 404 EC/IC**  
**B.E.II (IC) 4th semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	2
Examination Scheme Marks	100	25	Cont. Evaluation 20 Examination 30

1	FET : Introduction to theory and operations of n-channel JFET & MOSFET; Reversibility of drain & source; P-channel FET; FET switch; MOSFET inverter; Bias stability in FET; Different FET configuration; Small signal analysis of FET.
2	FREQUENCY RESPONSE : R-C, Coupled amplifier with BJTs; Effect of emitter bypass capacitor; Coupling capacitor of base and collector; Low frequency analysis of FET amplifier; Source bypass capacitor; Drain coupling capacitor and gate coupling capacitor.
3	HIGH FREQUENCY ANALYSIS OF TRANSISTOR AMPLIFIER : Transistor amplifier at high frequencies; Hybrid $\pi$ equivalent circuit at high frequency; High frequency behavior of CE & CC amplifier; FET at high frequencies; CD and CS amplifier at high frequency; GBW products of above circuits.
4	NEGATIVE FEEDBACK IN AMPLIFIERS : Basic concept of feedback amplifier; Effect on gain due to feedback; Input and output impedance; Feedback amplifiers and sensitivity function; Voltage series, voltage shunt, current series and current shunt configuration circuits; Details analysis and introduction to its design; Frequency response of a feedback amplifier.
5	OSCILLATORS : Barkhausen's criteria for oscillators; Stability concept; Three pole amplifier; Nyquist criteria; Stabilizing networks; Frequency compensation and sinusoidal oscillator; Phase shift, Wein bridge, Colpitts, Hartley, Crystal and Tune circuit type oscillators (AF & RF Range).
6	TUNED AMPLIFIER : Introduction to single tuned amplifier; G.B.W. response calculations & design; Cascade amplifier; Neutralization methods; Synchronously tuned amplifier; Elementary treatment of stagger tuned and doubly tuned amplifiers.

**REFERENCE :**

1. SCHILLING & BELOVE : Electronic Circuits - Discrete and Integrated, McGraw Hill Publication, 3rd edition 1989, reprint 1994.
2. MILLMAN & HALKIAS : Integrated Electronics, McGraw Hill Publication, 1992
3. MILLMAN & GRABEL : Micro Electronics, McGraw Hill Publication, 1992.
4. BOYLESTAD & NASHLESKY : Electronic Devices & Circuit Theory, 5th edition PHI, 1992, Ninth reprint 1995.
5. JAEGER RICHARD: Microelectronic Circuit Design, McGraw Hill (International Ed) 1996.
6. HORENSTEIN : Microelectronics Circuits & Devices, Prentice-Hall of India, 1996.

**PROPERTIES OF ENG. MATERIALS: IC 403 IC  
BE II ( IC ) 3<sup>rd</sup> Semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	0	0
Examination Scheme Marks	100	0	Cont. Evaluation 0 Examination 0

1	INTRODUCTIONS TO ATOMIC STRUCTURES. -Nature & Structure of atom,-Interatomic and inter molecular forces,-Different types of crystal structures.
2	FRICITION, LUBRICATION AND WEAR -Basic principles, -Effects, -requirement of lubrication, different type of friction and removing techniques
3	ELECTRONICS AND MECHANICAL PROPERTIES -Elasticity , Plasticity, flow strength and fracture- Viscosity,- Hardness of material, - Strain & Stress of different material, - dielectric properties,-radiation interaction .
4	MAGNETIC AND THERMAL PROPERTIES. -Basic concepts of magnetic properties-magnetization curve-hysteresis and magnetic material- formation of oxide field and mechanism of growth of field-thermal expansion , conductivity, radiation and protection.
5	BASIC MECHANISM OF CORROSION. -Corrosion and it's Protecting coating-Erosion corrosion-Ferrous metal, cast-iron, steel and alloy material and effect of alloying material, Corrosion reactions and control.
6	EFFECT OF DIFFERENT GASES & LIQUID ON MATERIAL -Properties of gas & liquid material, -Effect on different material, -Alloy & Composite material for selection of flow, temp., level, pressure etc. transducers according to fluid characteristic
7	METALS : Forming processes, Ferrous metals, Nonferrous metals and alloys, effect of high temperatures on metals , effects of gases on metals.

REFERENCES :

- 1 ZBIGNIEW D.JASTRAZEBSKI The nature and Properties of Engg. Material. , 3rd Edition John Wiley & sons,(1987)
- 2 VAN VALCK: Element of Material Science and Engg. Addison Publication (1989)

**PULSE AND SWITCHING CIRCUITS: EC 402 IC**  
**B.E.II IC 4th Semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	2
Examination Scheme Marks	100	25	Cont. Evaluation 20 Examination 30

1.	<p><b>LINEAR WAVE SHAPING:</b>                      .High pass RC Circuit and its response to sinusoidal step, Pulse , Square wave , Exponential and ramp inputs, High pass RC circuit as differentiator , Double Differentiation , Low Pass RC circuit and its response to sinusoidal , Step Pulse , Square wave , Exponential and ramp inputs, Low Pass RC circuit as integrator, Attenuators.                      Introduction-pulse nomenclature-effect of capacitance on pulse shape-introduction to pulse transformers-delay lines.</p>
2.	<p><b>CLAMPING AND COMPARATOR CIRCUIT:</b>                      Diode clippers- Transistor clipper- Clipping at two independent level- Emitter- Coupled clipper- Compensation for temperature changes in diode-Comparators-Applications of voltage comparators.</p>
3.	<p><b>CLAMPING AND SWITCHING CIRCUIT :</b>                      clipping, clamping operation - Clamping circuit- Clamping circuit theorem- Practical Clamping circuits-Effect of diode characteristics-Clamping in base - Synchronous clamping circuit- Transistor as a switch - Two stage overdriven amplifier- Damper diodes- Switch with inductive and capacitive load - Collector catching diode- Nonsaturating switches-Emitter follower with capacitive load..</p>
4.	<p><b>BISTABLE MULTIVIBRATORS:</b>                      Fixed biased transistor binary- Self biased transistor binary-Commutating capacitors-Methods of improving resolution- Methods of triggering-Schmitt trigger.</p>
5.	<p><b>MONOSTABLE AND ASTABLE MULTIVIBRATORS:-</b>                      Collector-Coupled monostable multivibrator-Emitter- Coupled monostable multivibrator- Gate width calculation and waveforms in Emitter- Coupled monostable- Triggering of monoshot- Astable collector coupled and emitter coupled multivibrator.</p>
6.	<p><b>SYNCHRONIZATION AND FREQUENCY DIVISION:</b>                      General features of a time-Base signal- Exponential sweep circuit-Pulse Synchronization of relaxation devices- Frequency division in sweep circuit- Astable and Monostable multivibrators as Frequency dividers.</p>
7.	<p><b>SWEEP CIRCUITS :</b>                      Time base generators-sweep generators-Miller integrator-Bootstrap sweep generator-current time base generator</p>
8.	<p><b>SYNCHRONIZATION AND FREQUENCY DIVISION :</b>Fundamental ideas about synchronization and frequency division.</p>

**TUTORIALS:** Tutorials shall be based upon theory.

**REFERENCES :**

1. MILLMAN & TAUB : Pulse, Digital and Switching waveforms, McGraw Hill, IS Edition,1978.
2. TAUB & SCHILLING : Digital Integrated Electronics, McGraw Hill, IS Edition,1977.
3. DOYLE J. : Pulse Fundamentals,2/e,1973.
4. MITCHELL B.B. : Semiconductor pulse circuits with experiments, Holt-Rhinehart & Winston,1970.
5. BELL DAVID A : Solid State Pulse Circuits, Reston Publishing Company,1976.
6. STRAUSS L. : Waveform generation and shaping,McGraw Hill,2/e.1970.
7. AGGARWAL & SURESH : Waveshaping and Digital Circuits, Khanna Publishers,1978
8. PASCOE.LOBERT D. : Solid State Switching-Discrete and Integrated, John Wiley and Sons,1973.

**CONTROL THEORY: IC 405 IC**  
**B.E. II IC 4th Semester**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	0
Examination Scheme Marks	100	25	Cont. Evaluation 00 Examination 00

1.	<b>BASIC CONCEPTS OF CONTROL LOOPS:</b> -Open loop control system with examples, Close loop control system With examples and it's merits over open loop system Effect of both on stability, gain And Speed response of system,
2.	<b>CONTROL LOOP BLOCK- DIAGRAMS:</b> Block-diagram representation from real system in form of T.F.,Block-diagram reduction techniques, Signal flow diagram tech. and it's advantages, Voltage and current Analogies for mechanical model.
3.	<b>ROOT LOCUS TECHNIQUES:</b> Plot Locies of root from transfer function, Stability criteria and system response study from root locus.
4.	<b>DIFFERENT PLOTS :</b> Bode plots and effect of gain margin, phase margin on system parameters, Polar plot and stability criteria, Nyquist plot and Hurwitz criteria, Nichol chart and it's application.
5.	<b>LAPLACE TRANSFORMATION TECHNIQUES:</b> Laplace transformation of given system, Inverse laplace transform, Brief ideas about Z-transformation and inverse Z-transformation tech., Use of L.T. in different order time response tech.,
6.	Concepts of stability & different stability criteria for system.

**TUTORIALS:** Tutorials shall be based upon theory.

**REFERENCES:**

1. NAGRATH & GOPAL : Control system engineering., New age International Publication (1996)
2. B.C. KUO : Automatic control systems, Prentice Hall of India Ltd, 1995
3. OGATA,KATSUHICO : Modern Control Engineering, PHI,1996

**DIGITAL INTEGRATED CIRCUIT: EC 406 IC**  
**B.E. II (IC) 4th Semesters**

	Lecture	Tutorial	Practical
Teaching Hours	3	1	2
Examination Scheme Marks	100	25	Cont. Evaluation 20 Examination 30

1.	RTL and IIL Circuits: RTL logic gates and DCTL gates, current hogging and Fan, out and Fan, in RTL and DCTL, RTL buffer, Manufacturer's specifications, concept and physical layout of IIL.
2.	DTL and TTL Circuits: DTL and TTL gates, High threshold logic, input, output characteristics of DTL and TTL gates, wired AND connection, active pull, up, Schottky TTL and other logic with TTL gates.
3.	Emitter, coupled logic and MOS gates :Transfer characteristics fan, out, speed of operation, logic versatility of ECL gates, ECL gates interconnection, MOS and CMOS inverters, Rise time and fall time in CMOS gates, Manufacturer's specification, interfacing BJT and CMOS Gates.
4.	BiCMOS Logic Circuits: Introduction, BJT structure and operation, Dynamic behavior of BJT, Basic BiCMOS circuits, Static behavior, Switching delay in BiCMOS switching circuits, BiCMOS applications.
5.	Circuit Design for LSI and VLSI: CMOS and Bipolar Transistor Gate Arrays and their limitations, Standard Cell, Programmable Logic Array, Circuit Design for VLSI
6.	Semiconductor Memories: Types of memories, Implementation of ROM, MOS ROM cells, MOS EPROM and EEPROM, applications, Static and Dynamic Read, Write memories, Organization of RAM, Paralleling of Semiconductor Memory Integrated Circuit chips

TUTORIALS: Tutorials shall be based upon theory.

REFERENCES:

1. TAUB AND SCHILLING: Digital Integrated Electronic McGraw Hill.(1985)
2. TEXAS INSTRUMENTS: Designing with TTL Integrated circuits McGraw Hill(1984).
3. HODGES & JACKSON: Introduction to Integrated Electronics circuits, McGraw Hill, 1/e, 1992
4. MILLMAN : Microelectronics, McGraw Hill(1988).
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